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703	15
Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

**U.S. UTILITY Patent Application**

O.I.P.E. TR PD <sup>3</sup> AA SCANNED Q.A.	PATENT DATE
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APPLICATION NO. 09/726114	CONT/PRIOR F	CLASS 251 <sup>703</sup> <del>412</del>	SUBCLASS <del>13</del> <sup>15</sup>	ART UNIT <sup>2123</sup> <del>2811</del> <del>2826</del>	EXAMINER <del>2121</del> CRAIG
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Shigetaka Kumashiro

Apparatus and method for simulating MOSFET

PTO-2040  
12/99

<b>ISSUING CLASSIFICATION</b>									
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>					
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>				
<b>INTERNATIONAL CLASSIFICATION</b>									

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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